

Notice of Allowability

Application No.

10/775,328

Examiner

Steven J. Fulk

Applicant(s)

KATO ET AL.

Art Unit

2891

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address--

All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. **THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS.** This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.

1. ☒ This communication is responsive to the amendment filed 5/7/07.
2. ☒ The allowed claim(s) is/are 1-11 and 16-27.
3. ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
 - a) ☒ All b) ☐ Some* c) ☐ None of the:
 1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).

* Certified copies not received: _____.

Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.
THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.

4. ☐ A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.
5. ☐ CORRECTED DRAWINGS (as "replacement sheets") must be submitted.
 - (a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached
 - 1) ☐ hereto or 2) ☐ to Paper No./Mail Date _____.
 - (b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date _____.

Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).
6. ☐ DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.

Attachment(s)

1. ☐ Notice of References Cited (PTO-892)
2. ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
3. ☐ Information Disclosure Statements (PTO/SB/08),
Paper No./Mail Date _____
4. ☐ Examiner's Comment Regarding Requirement for Deposit
of Biological Material
5. ☐ Notice of Informal Patent Application
6. ☐ Interview Summary (PTO-413),
Paper No./Mail Date _____
7. ☐ Examiner's Amendment/Comment
8. ☒ Examiner's Statement of Reasons for Allowance
9. ☐ Other _____

DETAILED ACTION

Response to Arguments

1. Applicant's amendment filed April 27, 2007, which amended claims 1-11 and 22, canceled claims 12-15, and added claims 23-27, has been entered. Applicant's supplemental amendment filed May 7, 2007, which amends claims 4 and 8, has also been entered. Claims 1-11 and 16-27 are currently pending.
2. Applicant's arguments with respect to the rejection of independent claims 1-10 have been fully considered and are persuasive. The rejection of claims 1-11 and 16-22 has been withdrawn.

Allowable Subject Matter

3. Claims 1-11 and 16-27 are allowed.
4. The following is an examiner's statement of reasons for allowance: a search of the prior art failed to disclose or reasonably suggest a semiconductor device, or electronic equipment comprising the semiconductor device, comprising: a first semiconductor element including at least one thin film transistor and a light emitting element electrically connected to the first semiconductor element; a second semiconductor element including at least one thin film transistor and a light receiving element electrically connected to the second semiconductor element; wherein the first semiconductor element and the second semiconductor element are stacked and a resin film is formed between the first semiconductor element and the second semiconductor element; wherein the light emitting element comprises a first electrode, a second electrode, and an electro-luminescent layer formed between the first electrode and the second electrode, and wherein the first electrode, the

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electro-luminescent layer, and the second electrode are overlapped each other, as recited by claims 1-11.

Ovshinsky et al. '471 discloses a semiconductor device (figs. 6A & 6B; col. 18, line 58 – col. 19, line 66) comprising a stacked thin film light emitting element (layer 158 of device 232) and a light receiving element (layer 42 of device 234), with an adhesive film (leveling film, 220) and an insulating film (236) formed between semiconductor circuits, but the reference does not teach the light emitting element and the light receiving element to each be electrically connected to a thin film transistor, nor does the reference teach the stacked elements to have a resin formed between them.

Kohashi '056, Thillays '877, Miyoshi et al. '821, Stein '695, Yasumoto et al. '083, Lebby et al. '245, Vu et al. '953, Hayashi et al. '699, Spaeth et al. '559, and Haas et al. '214 disclose stacked optoelectronic coupling devices, but the references do not teach the light emitting element and the light receiving element to each be electrically connected to a thin film transistor, nor does the reference teach stacked elements having a resin formed between them.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

Conclusion

5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven J. Fulk whose telephone number is (571)

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272-8323. The examiner can normally be reached on Monday through Friday, 9:30am to 6:00pm.

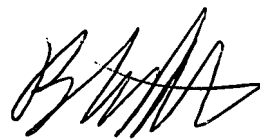
If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Bill Baumeister can be reached on (571) 272-1722. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

6. Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free). If you would like assistance from a USPTO Customer Service Representative or access to the automated information system, call 800-786-9199 (IN USA OR CANADA) or 571-272-1000.

SJF

Steven J. Fulk
Patent Examiner
Art Unit 2891

June 22, 2007



**B. WILLIAM BAUMEISTER
SUPERVISORY PATENT EXAMINER**